

Abstract Of The Disclosure

A method for activating a microprocessor, which is part of a microcontroller, within the framework of a boundary scan test procedure according to Institute of Electrical and Electronic Engineers (IEEE) standard 1149, using a Joint European Test Action Group (JTAG) interface of the microprocessor. To be able to test a microcontroller, using the boundary scan test procedure, even when the JTAG interface is not accessible by a separate hardware adaptor of a JTAG tester, it is proposed that the JTAG interface of the microprocessor be activated by a test routine executable on the microprocessor.

5